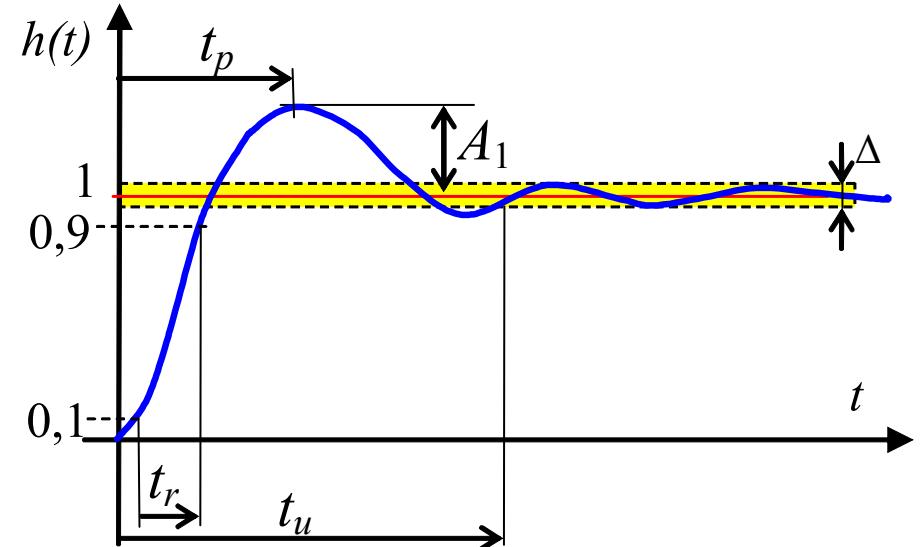
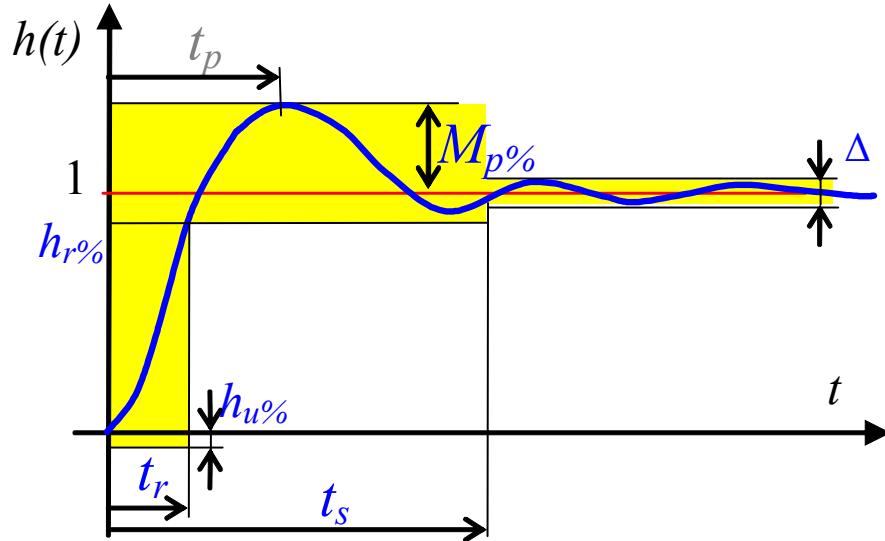


Bezpośrednie wskaźniki jakości



e_s , e_k - uchyb ustalony (końcowy); Δ – tolerancja uchybu

A_1 - przeregulowanie (e_1 - max uchyb dynamiczny)

t_p - czas pierwszego przeregulowania; t_r - czas narostu; t_u - czas regulacji (czas ustalania)

e_s – steady-state error; Δ - precent settling, e.g. $= \pm 1\%$ ($\pm 5\%$)

M_p – overshoot ($M_p\%$ - precent overshoot, e.g. $= 20\%$);

t_p - peak time; t_r – rise time; t_s – settling time

$h_r\%$ - precent rise, e.g. $= 90\%$; $h_u\%$ - precent undershoot, e.g. $= 1$

Simulink Optimization Design - uruchomienie

Matlab + Simulink + Optimization + Simulink Optimization Design
[lub + Simulink Control Design]

The screenshot shows the MATLAB interface with the Simulink Optimization Design tool highlighted.

Libraries:

- Simulink
- Commonly Used Blocks
- Continuous
- Discontinuities
- Discrete
- Logic and Bit Operations
- Lookup Tables
- Math Operations
- Model Verification
- Model-Wide Utilities
- Ports & Subsystems
- Signal Attributes
- Signal Routing
- Sinks
- Sources
- User-Defined Functions
 - + Additional Math & Discrete
- Aerospace Blockset
- Communications System Toolbox
- Computer Vision System Toolbox
- Control System Toolbox
- DSP System Toolbox
- Fuzzy Logic Toolbox
- Image Acquisition Toolbox
- Instrument Control Toolbox
- Model Predictive Control Toolbox
- Neural Network Toolbox
- Report Generator
- Robust Control Toolbox
- SimRF
- Simscape
- Simulink 3D Animation
- Simulink Coder
- Simulink Control Design
- Simulink Design Optimization**
- Simulink Extras
- Simulink Verification and Validation
- Stateflow
- System Identification Toolbox

Tools menu:

- Simulink Debugger...
- Model Advisor...
- Model Dependencies
- Fixed-Point Tool...
- Lookup Table Editor...
- Define Data Classes...
- Bus Editor...
- Profiler
- Coverage Settings
- Requirements
- Inspect Logged Signals...
- Signal & Scope Manager...
- Code Generation
- External Mode Control Panel...
- Control Design
- Parameter Estimation...
- Response Optimization...**
- Report Generator...
- Compare Simulink XML Files...
- Data Object Wizard
- MPlay Video Viewer
- Run on Target Hardware

Diagram:

```
graph LR; Step[Step] --> Add((Add)); Add --> Scope[Scope];
```

The diagram shows a simple model with a Step input block, an Add block, and a Scope output block.

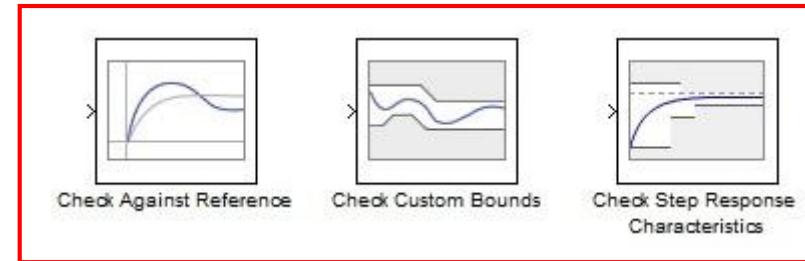
Simulink Optimization Design - bloki

Enter search term

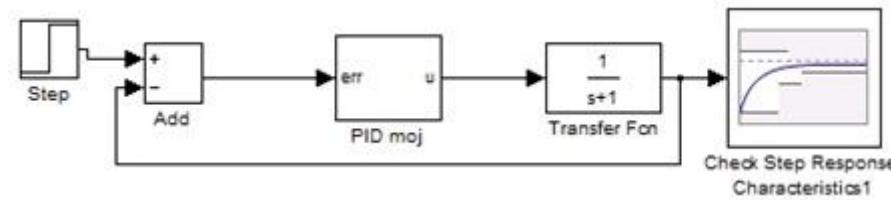
Libraries

- Simulink
 - Commonly Used Blocks
 - Continuous
 - Discontinuities
 - Discrete
 - Logic and Bit Operations
 - Lookup Tables
 - Math Operations
 - Model Verification
 - Model-Wide Utilities
 - Ports & Subsystems
 - Signal Attributes
 - Signal Routing
 - Sinks
 - Sources
 - User-Defined Functions
 - + Additional Math & Discrete
- Aerospace Blockset
- Communications System Toolbox
- Computer Vision System Toolbox
- Control System Toolbox
- DSP System Toolbox
- Fuzzy Logic Toolbox
- Image Acquisition Toolbox
- Instrument Control Toolbox
- Model Predictive Control Toolbox
- Neural Network Toolbox
- Report Generator
- Robust Control Toolbox
- SimRF
- Simscape
- Simulink 3D Animation
- Simulink Coder
- Simulink Control Design
- + Simulink Design Optimization
- Simulink Extras
- Simulink Verification and Validation
- Stateflow
- System Identification Toolbox

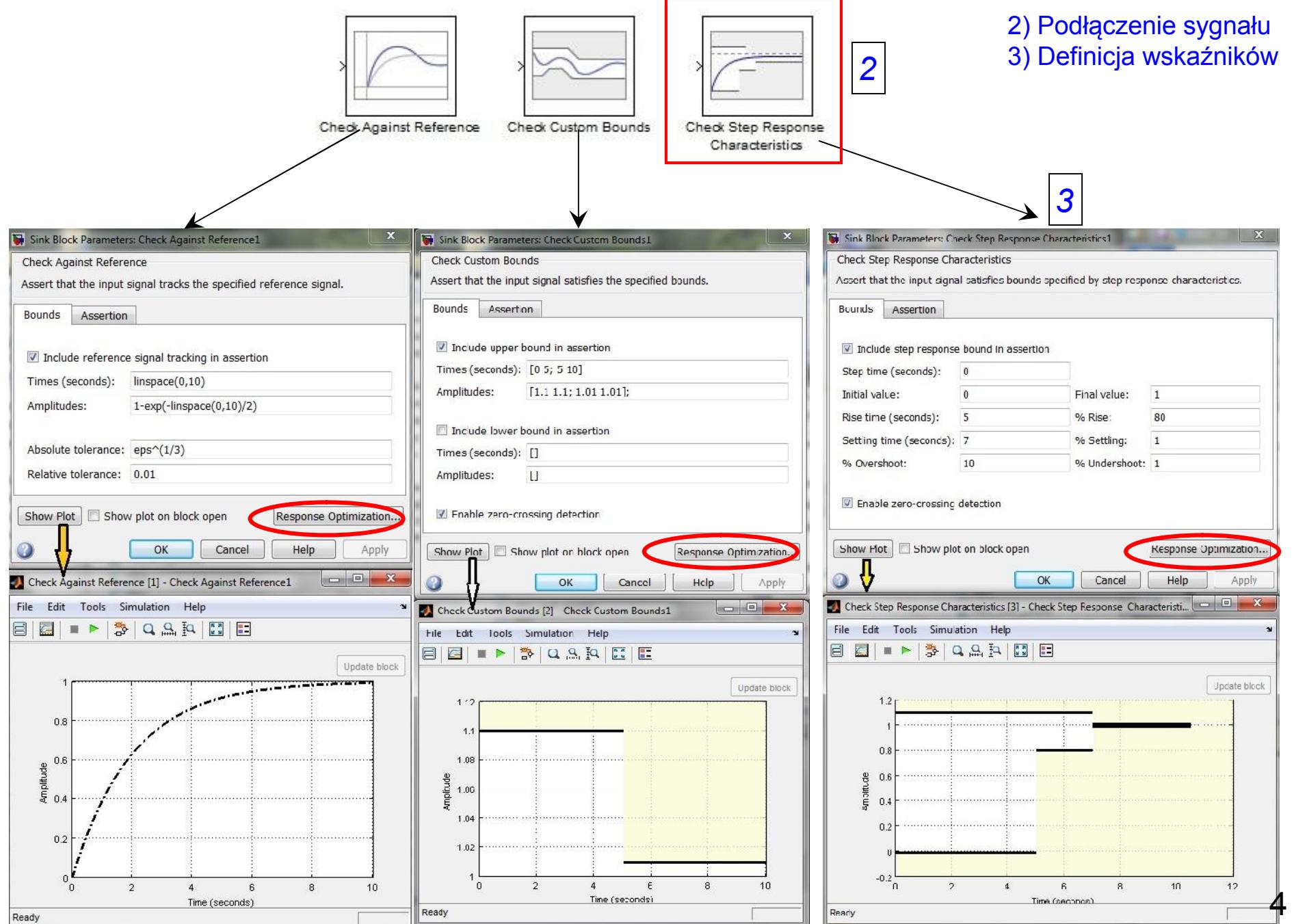
Model Verification



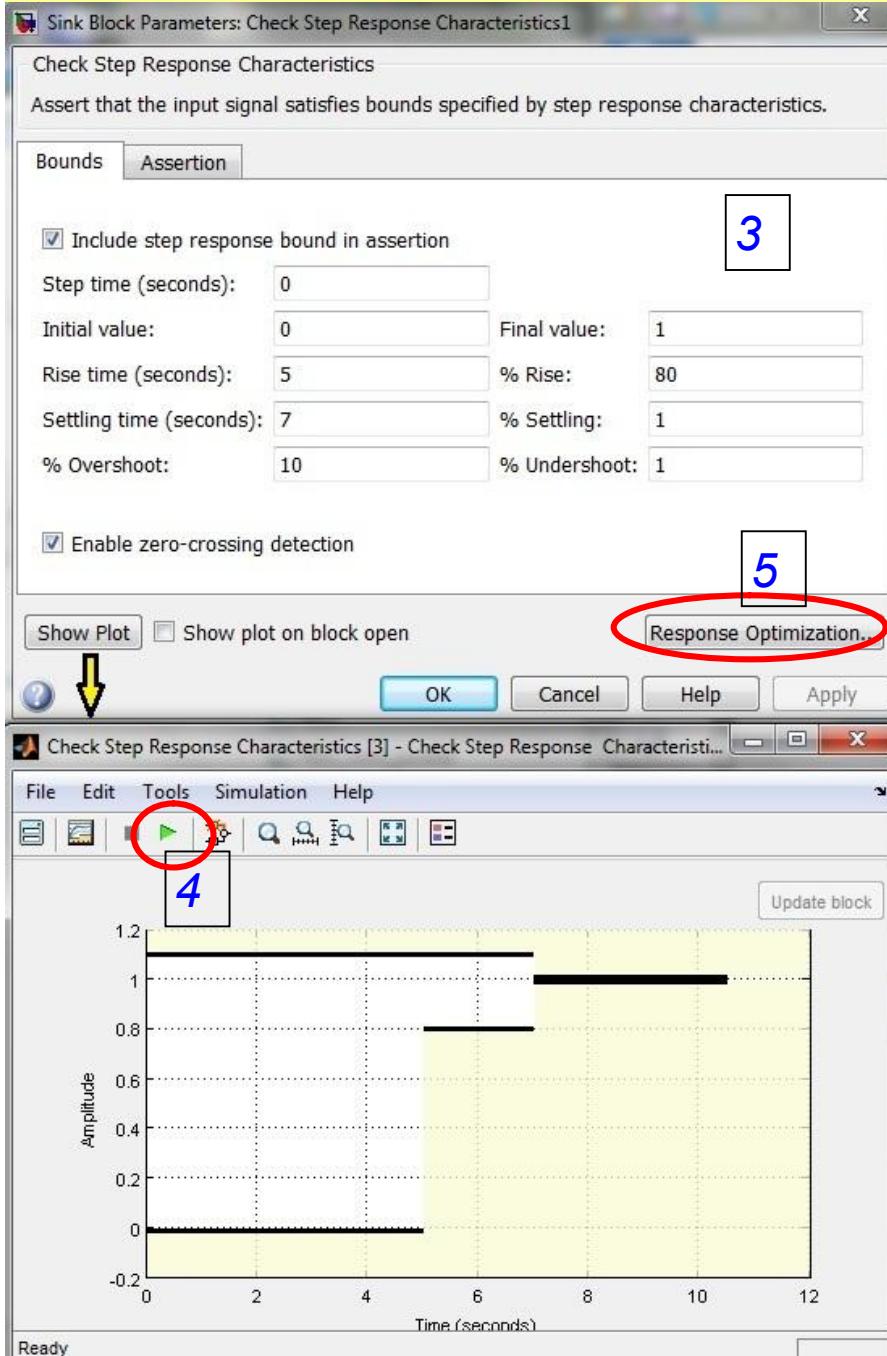
- 1) Wybór bloku
- 2) Podłączenie sygnału



Simulink Optimization Design - bloki



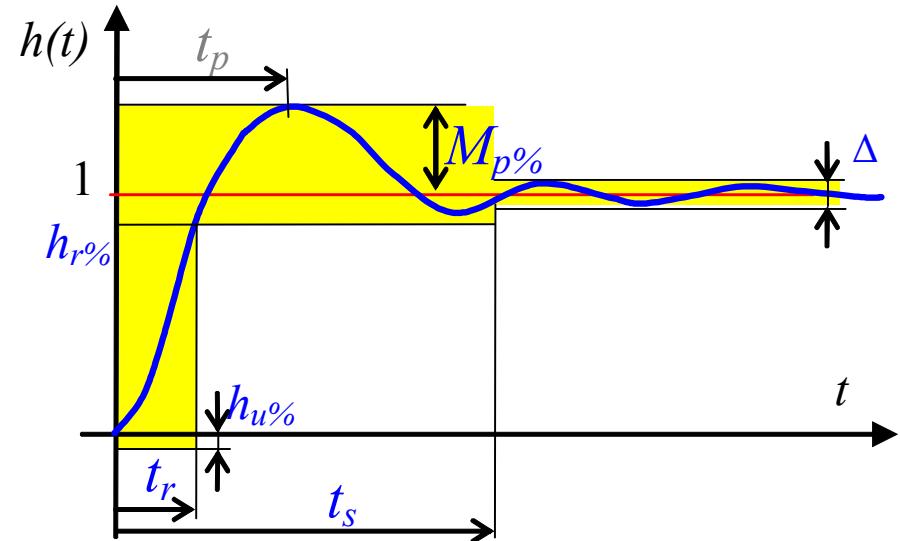
Simulink Optimization Design – blok Step Response



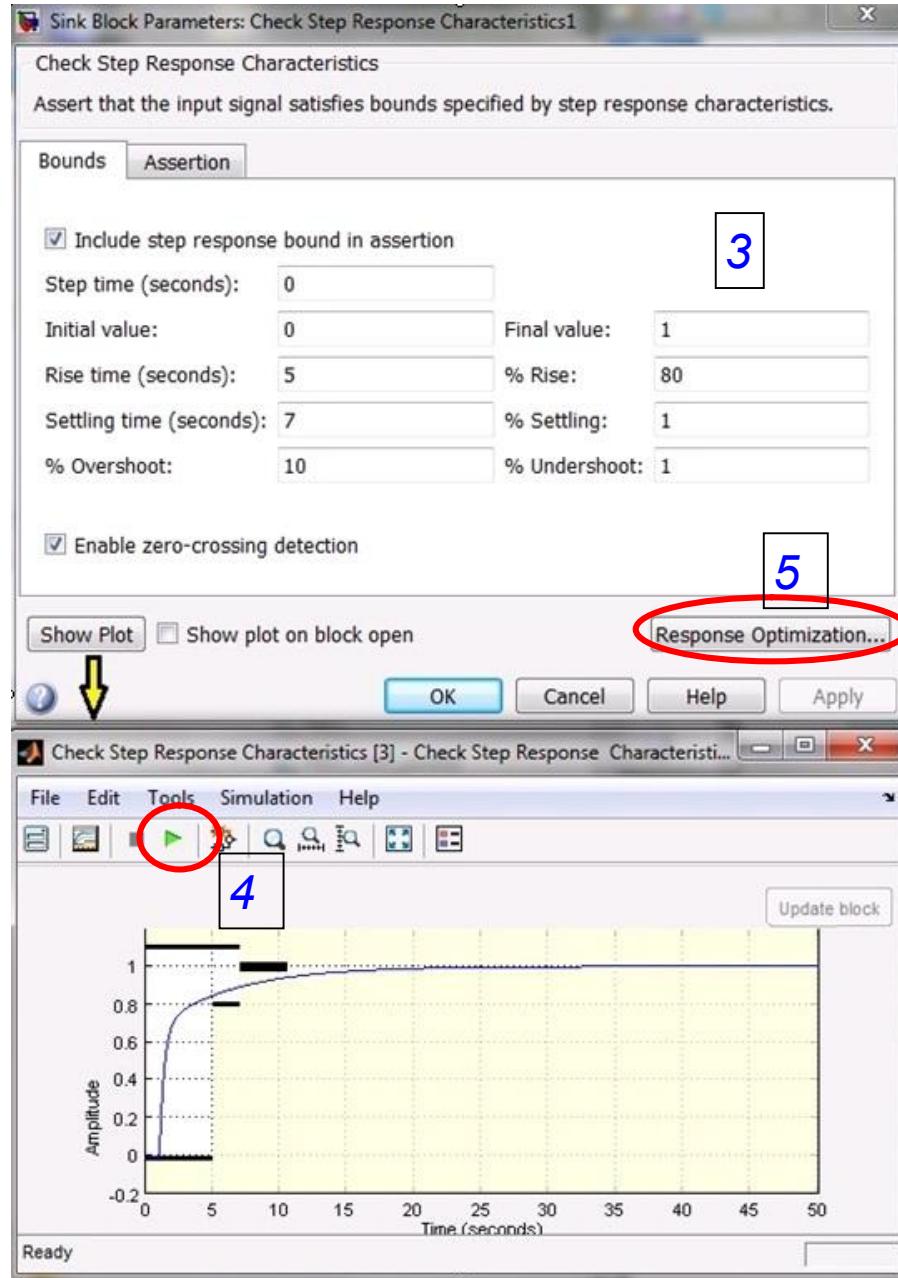
- 3) Definicja wskaźników
- 4) Sprawdzenie

- | | |
|--------------------|----------------|
| Settling time | $t_s = 3$ |
| Percent settling | $\Delta = 1$ |
| Percent overshoot | $M_{p\%} = 20$ |
| Rise time | $t_r = 1$ |
| Percent rise | $h_{r\%} = 90$ |
| Percent undershoot | $h_{u\%} = 1$ |

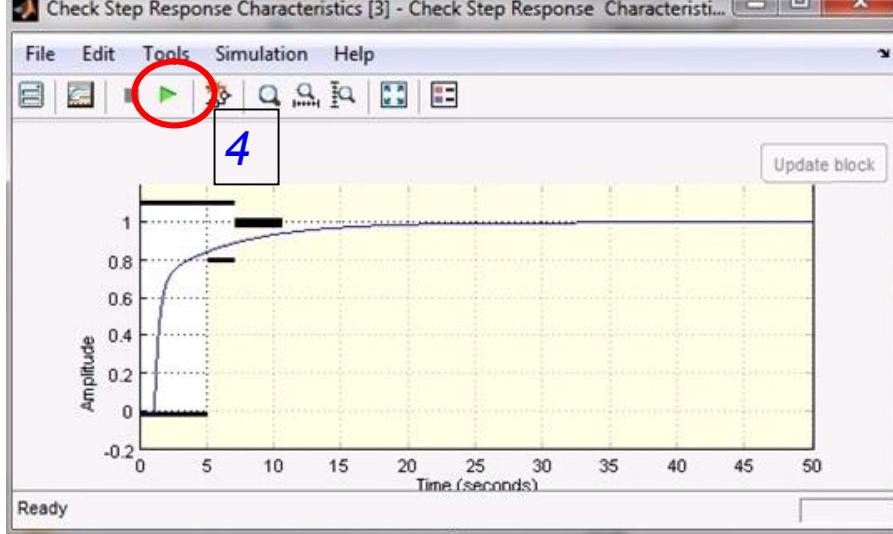
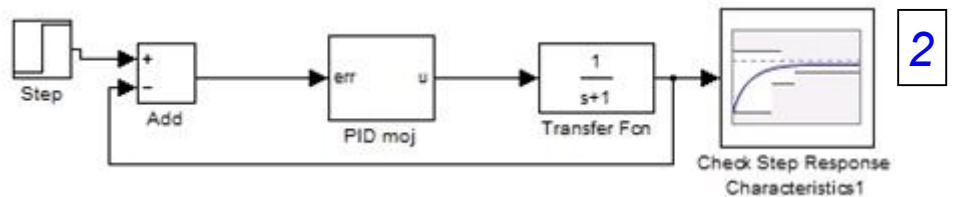
Step time = t_0 Final time = t_k
 Initial output = h_0 Final output = h_k



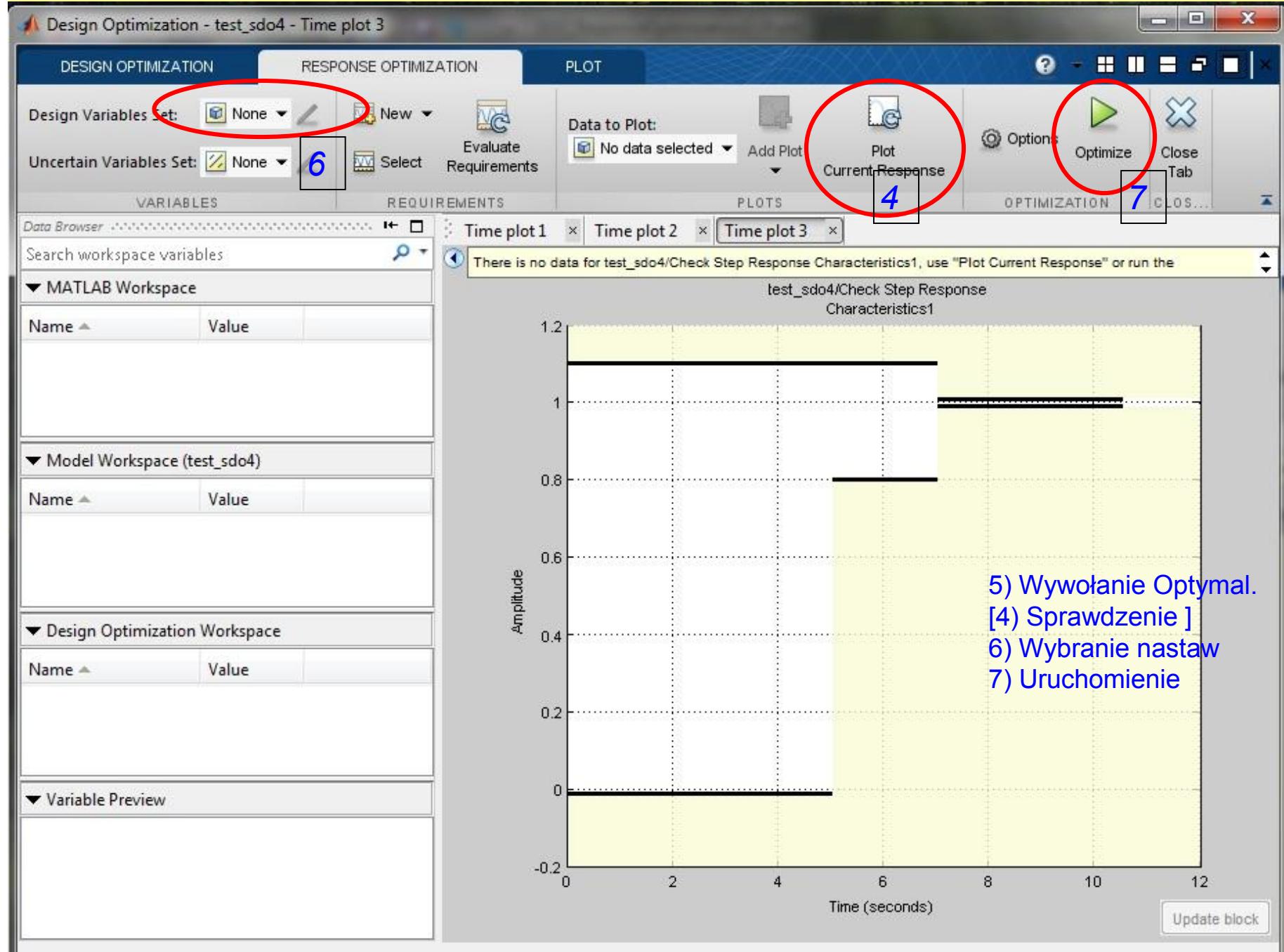
Simulink Optimization Design – Response Optimization



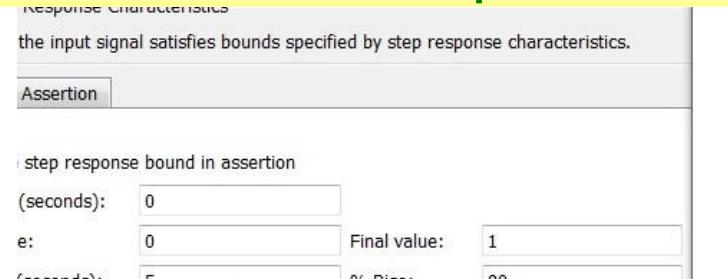
- 4) Sprawdzenie
- 5) Wywołanie Optymal.



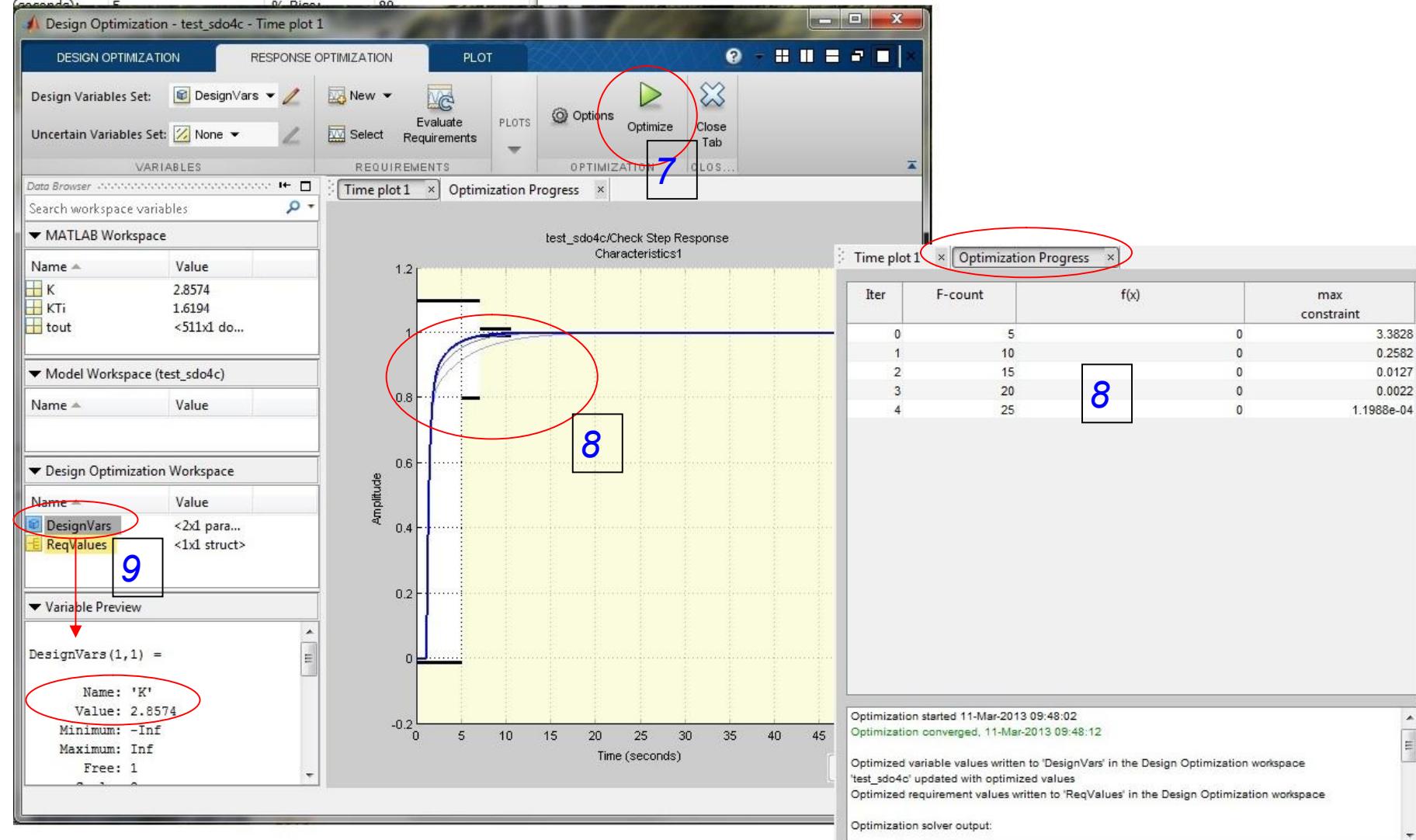
Simulink Optimization Design – Response Optimization



Simulink Optimization Design – Response Optimization



- 7) Uruchomienie
- 8) Praca Optymalizatora
- 9) Wyniki



Simulink Optimization Design – Response Optimization

Simulink model: `test_sdo4c`

Block diagram:

```
graph LR; Step[Step] --> Add[Add]; Add --> PID[PID mAj]; PID --> Transfer[Transfer Fcn]; Transfer --> Check[Check Step Response Characteristics1]
```

Block parameters:

- Check Step Response Characteristics1**:
 - Step time (seconds): 0
 - Initial value: 0
 - Final value: 1
 - Rise time (seconds): 5
 - % Rise: 80
 - Settling time (seconds): 7
 - % Settling: 1
 - % Overshoot: 10
 - % Undershoot: 1
 - Include step response bound in assertion
 - Enable zero-crossing detection
 - Show Plot
 - Show plot on block open
- Design Optimization - test_sdo4c - Time plot 1**:
 - DESIGN OPTIMIZATION tab selected.
 - Design Variables Set: DesignVars (highlighted with red circle 6).
 - Uncertain Variables Set: None (highlighted with red circle 6).
 - PLOTS tab selected.
 - OPTIMIZATION tab selected.
 - Time plot 1: Amplitude vs Time (seconds). The plot shows a step response starting at t=0 with an overshoot of approximately 10% and settling at t=7. The plot area is highlighted with a red circle 7.
 - Data Browser: Shows workspace variables K=2, KTi=0.5000, tout=<511x1 do...>.
 - Model Workspace (test_sdo4c): Shows workspace variables.
 - Design Optimization Workspace: Shows workspace variables.
 - DesignVars <2x1 para...> (highlighted with red circle 9).
 - val(1,1) =
 - Name: 'K'
 - Value: 2
 - Minimum: -Inf
 - Maximum: Inf
 - Free: 1
 - Scale: 2
 - Info: [1x1 struct]
 - val(2,1) =
 - Name: 'KTi'
 - Value: 0.5000
 - Minimum: -Inf
 - Maximum: Inf
 - Free: 1
 - Scale: 0.5000
 - Info: [1x1 struct]